Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-559001	Application No. 10/055,483
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Jared W. Stark IV et al.	
		Filing Date January 22, 2002	Group Art Unit

U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date, If Appropriate
	AA						
	AB						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation Yes	Translation No
	AC							
	AD							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
DJH	AE	Ramon Canal et al., "A Low-Complexity Issue Logic", ICS 2000 Santa Fe, New Mexico, pgs. 327-335
DJH	AF	James A. Farrell et al., "Issue Logic for a 600-MHz Out-of-Order Execution Microprocessor", IEEE 1998, pgs. 707-712
DJH	AG	Dana S. Henry et al., "Circuits for Wide-Window Superscalar Processors", ISCA 2000 Vancouver BC Canada, pgs. 236 – 247
DJH	AH	Glenn Hinton et al., "The Mircroarchitecture of the Pentium® 4 Processor", Intel Technology Journal Q1, 2001, pgs. 1-13
DJH	AI	Pierre Michaud et al., "Data-Flow Prescheduling for Large Instruction Windows in Out-of-Order Processors", IEEE 2001, pgs. 27 – 36
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DJH	AR	IA-32 Intel Architecture Software Developer's Manual Volume 1: Basic Architecture, Intel Corporation, 2001

Examiner Signature	Date Considered
David J. Huynh	4-13-04

EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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DJH	AS	John Paul Shen, "Replenishing the Microarchitecture Treasure Chest", Carnegie-Mellon University, 1999
DJH	AT	James E. Thornton, " Design of a Computer: The Control Data 6600", Foresman Press, 1970
DJH	AU	R.M. Tomasulo, "An efficient algorithm for exploiting multiple arithmetic units", IBM Journal of Research and Development, Vol. 11, pgs. 25-33, January 1967

Examiner Signature	Date Considered
David J. Huisman	8-13-04

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